# IBM 701

The **IBM 701 Electronic Data Processing Machine**, known as the **Defense Calculator** while in development, was <u>IBM</u>'s first commercial scientific computer and its first series production mainframe computer, which was announced to the public on April 29, 1952.<sup>[1]</sup> It was invented and developed by <u>Jerrier</u> <u>Haddad</u> and <u>Nathaniel Rochester</u> based on the <u>IAS machine</u> at Princeton.<sup>[2][3][4]</sup>

The IBM 701 was the first computer in the <u>IBM 700/7000 series</u>, which was responsible for bringing electronic computing to the world and for IBM's dominance in the mainframe computer market during the 1960s and 1970s that continues today.<sup>[5]</sup> The series were IBM's high-end computers until the arrival of the <u>IBM</u> System/360 in 1964.<sup>[5]</sup>

The business-oriented sibling of the 701 was the <u>IBM 702</u> and a lower-cost general-purpose sibling was the famous <u>IBM 650</u>, which became the first mass produced computer in the world. [4][6]

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## History

IBM 701 competed with Remington Rand's <u>UNIVAC 1103</u> in the scientific computation market, which had been developed for the

NSA, so it was held secret until permission to market it was obtained in 1951.<sup>[7]</sup> In early 1954, a

### IBM 701 Electronic Data Processing Machine



Successor

1024 bits each)

**IBM 704** 

committee of the <u>Joint Chiefs of Staff</u> requested that the two machines be compared for the purpose of using them for a Joint Numerical Weather Prediction project.

Based on the trials, the two machines had comparable computational speed, with a slight advantage for IBM's machine, but the UNIVAC was favored unanimously for its significantly faster input-output equipment.<sup>[8]</sup> At the Lawrence Livermore National Laboratory, an IBM 701 meant that scientists could run nuclear explosives computations faster.

Nineteen IBM 701 systems were installed.<sup>[9]</sup> The first 701 was delivered to IBM's world headquarters in New York. Eight went to aircraft companies.

"I think there is a world market for maybe five computers" is often attributed to Thomas Watson, Senior in 1943 and Junior at several dates in the 1950s. This misquote is from the 1953 IBM annual stockholders' meeting. <u>Thomas Watson Jr</u>. was describing the market acceptance of the IBM 701 computer. Before production began, Watson visited with 20 companies that were potential customers. This is what he said at the stockholders' meeting, "as a result of our trip, on which we expected to get orders for five machines, we came home with orders for 18".<sup>[10]</sup>

<u>Aviation Week</u> for 11 May 1953 says the 701 rental charge was about \$12,000 a month; *American* Aviation 9 Nov 1953 says "\$15,000 a month per 40-hour shift. A second 40-hour shift ups the rental to \$20,000 a month".

The successor of the 701 was the <u>index register</u>-equipped <u>IBM 704</u>, introduced 4 years after the 701. The 704 was not compatible with the 701, however, as the 704 increased the size of instructions from 18 bits to 36 bits to support the extra features. The 704 also marked the transition to <u>magnetic core</u> <u>memory</u>.

## **Social Impact**

In 1952 IBM paired with language scholars from <u>Georgetown University</u> to develop a translation software for use on computers. On January 7, 1954, the team developed an experimental software program that allowed the IBM 701 computer to translate from Russian to English. The Mark 1 Translating Device, which was developed for the US Air Force, was able to produce its first automated Russian-to-English translation in 1959 and was shown to the public in 1964.<sup>[11]</sup>

In 1954, a group of scientists ran millions of simulated hands of blackjack on an IBM 701 looking to determine the best playing decision for every combination of cards. The result of the study was the set of correct rules for hitting, standing, doubling or splitting in a blackjack game which are still the same today.<sup>[11]</sup>

The IBM 701 can claim to be the first computer displaying the potential of artificial intelligence in <u>Arthur Samuel</u>'s Checkers-playing Program on February 24, 1956. The program, which was developed for play on the IBM 701, was demonstrated to the public on television. Self-proclaimed checkers master Robert Nealey played the game on an IBM 7094 computer in 1962 and the computer won. It is still considered a milestone for artificial intelligence and it offered the public during the early 1960s an example of the capabilities of an electronic computer.<sup>[12]</sup>

The <u>University of California Radiation Laboratory at Livermore</u> developed a language compilation and runtime system called the <u>KOMPILER</u> for their IBM 701. <u>Speedcode</u> was the first <u>high-level</u> programming language created for an IBM computer. The language was developed by John Backus in 1953 for the IBM 701 to support computation with <u>floating point numbers</u>. The <u>Fortran</u> compiler also developed by Backus was not released by IBM until the IBM 704.

## IBM 701 customers

- IBM World Headquarters, New York, N.Y. (1952)
- University of California., Los Alamos, N.M. (1953)
- Lockheed Aircraft Company, Glendale, Cal. (1953)
- National Security Agency, Washington, D.C. (1953)
- Douglas Aircraft Company, Santa Monica, Cal. (1953)
- General Electric Company., Lockland, Ohio (1953)
- Convair, Fort Worth, Tex. (1953)
- U.S. Navy, Inyokern, Cal. (1953)
- United Aircraft, East Hartford, Conn. (1953)
- North American Aviation, Santa Monica, Cal. (1953)
- Rand Corporation., Santa Monica, Cal. (1953)<sup>[13]</sup>
- Boeing Corporation, Seattle, Wash. (1953)
- Douglas Aircraft Company, El Segundo, Cal. (1954)
- Naval Aviation Supply, Philadelphia, Pa. (1954)
- University of California, Livermore, Cal. (1954)
- General Motors Corporation, Detroit, Mich. (1954)
- Lockheed Aircraft Company, Glendale, Cal. (1954)
- U.S. Weather Bureau, Washington, D.C. (1955)
- Dupont Central Research, Wilmington, DE (1954)<sup>[14]</sup>

## Description

### Hardware configuration

The IBM 701 system<sup>[15]</sup> was composed of the following units:

- IBM 701 Analytical Control Unit (CPU)
- IBM 706 Electrostatic Storage Unit (2048 words of <u>Williams</u> <u>tube</u> Memory)
- IBM 711 Punched Card Reader (150 Cards/min.)
- IBM 716 Printer (150 Lines/min.)
- IBM 721 Punched Card Recorder (100 Cards/min.)
- IBM 726 Magnetic Tape Reader/Recorder (100 Bits/inch)
- IBM 727 Magnetic Tape Reader/Recorder (200 Bits/inch)
- IBM 731 Magnetic Drum Reader/Recorder
- IBM 736 Power Frame #1
- IBM 737 Magnetic Core Storage Unit (4096 words of 12 µs Core Memory)



IBM 701 processor frame, showing 1071 of the vacuum tubes

- IBM 740 Cathode Ray Tube Output Recorder
- IBM 741 Power Frame #2
- IBM 746 Power Distribution Unit
- IBM 753 Magnetic Tape Control Unit (controlled up to ten IBM 727s)

The total weight (depending on configuration) was about 20,516 pounds (10.3 short tons; 9.3 t).<sup>[16]</sup>



Vacuum tube logic module from a 700 series IBM computer.

### Memory

The system used <u>vacuum tube</u> logic circuitry and electrostatic storage, consisting of 72 <u>Williams tubes</u> with a capacity of 1024 <u>bits</u> each, giving a total memory of 2048 <u>words</u> of 36 bits each. Each of the 72 Williams tubes was 3 inches in diameter. Memory could be expanded to a maximum of 4096 words of 36 bits by the addition of a second set of 72 Williams tubes or (later) by replacing the entire memory with magnetic-<u>core memory</u>. The Williams tube memory and later core memory each had a memory cycle time of 12 microseconds. The Williams tube memory required periodic refreshing, mandating the insertion of <u>refresh cycles</u> into the 701's timing. An addition operation required five 12microsecond cycles, two of which were refresh cycles, while a multiplication or division operation required 38 cycles (456 microseconds).

Williams tube from an IBM 701 at the <u>Computer</u> <u>History Museum</u>

### Instruction set

Instructions were 18 bits long, single address.

- Sign (1 bit) Whole word (-) or Half word (+) operand address
- Opcode (5 bits) 32 instructions
- Address (12 bits) 4096 Half word addresses

Numbers were either 36 bits or 18 bits long, signed magnitude, fixed point. The full word has a precision of about ten decimal digits. A decimal digit corresponds to  $log_2 10$  or 3.322 bits.

The IBM 701 had only two programmer accessible registers:

- 1. The accumulator was 38 bits long (adding two overflow bits).
- 2. The multiplier/quotient was 36 bits long.

### Peripherals

The Magnetic Drum Reader/Recorder was added on the recommendation of John von Neumann, who said it would reduce the need for high speed I/O.<sup>[17]</sup>

The first magnetic tape drives were used on the Tape Processing Machine (TPM) and then adapted to the 701.<sup>[18]</sup>

## See also

- IBM 700/7000 series
- List of IBM products
- List of vacuum tube computers
- SHARE (computing)
- <u>Strela computer</u>, comparable Soviet design

## References

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What's included in total weight:

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## **External links**

- <u>A Notable First: The IBM 701 (http://www-1.ibm.com/ibm/history/exhibits/701/701\_intro.html)</u> (IBM Archives)

- Oral history interview with Gene Amdahl (http://purl.umn.edu/104341) Charles Babbage Institute, University of Minnesota, Minneapolis. Amdahl discusses his role in the design of several computers for IBM including the IBM 701, the IBM 704 and the STRETCH. He discusses his work with Nathaniel Rochester and IBM's management of the design process for computers.
- The Williams Tube (https://web.archive.org/web/20030216135550/http://www.computer50.org/kgill /williams/williams.html)

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